

Remarks

Claims 1, 2, 7-11, 13-15, 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by U. S. patent No. 6,187,032 to Ohyu. In light of the amendments above, we believe that Ohyu no longer anticipates the above-captioned application, and we request that these rejections be withdrawn.

As amended, these claims now include taking the source reconstruction results and using it to determine an appropriate latency range, and refiltering data at a different latency range based on the source reconstruction result. As seen in Ohyu's figure 7, the processing or filtering step is done in step 2 (S2) and is never adjusted based on the results of the source reconstruction.

One of the advantages of the present invention is its ability to test a trial set of data at several latencies in order to determine which latency will produce the best test results. It is also a significant improvement in the art to determine if changes in the test set-up are needed based on the source reconstruction results. Ohyu fails to teach or disclose this capability in his patent. Consequently, as currently amended, Ohyu fails to anticipate claims 1, 2, 7-11, and 12.

Claims 14, 15, 18, and 19 were rejected under 35 U.S.C. 102(b) as being anticipated by Ohyu. We respectfully request reconsideration of these claims.

The Office Action states that Ohyu discloses a processor configured to support multiple threads of execution in column 12, lines 4-20 of the patent. We respectfully

disagree. Ohyu does not disclose or teach of using a system with a multiple thread architecture. Multiple threads of execution represent a single sequence of instruction executed in parallel with other sequences, either by time slicing or multiprocessing. Multiple threads are able to share information, memory and other resources between each thread. The ability to perform multiple threads of execution is not taught or disclosed in Ohyu. Consequently, we request that claims 14, 15, 18, and 19 be allowed.

Claim 20 was also rejected under 35 U.S.C. 102(b) as being anticipated by Ohyu. As amended, the claim now includes using the source reconstruction to modify the acquisition of a new set of electromagnetic signals. As shown in Fig. 7 of Ohyu, the acquisition of the signal step S1 is not controlled by the source reconstruction results. As such we request that claim 20 be allowed.

Claims 3-6, 13, 16, and 17 were rejected under 35 U.S.C. 103(a) as being compatible over Ohyu in view of U.S. Patent No. 6,073,040 to Kiyuna.

With respect to claims 3-6, 13, as amended, these claims now include using the source reconstruction to determine an appropriate latency range. As argued above, we do not believe that this is taught or disclosed in Ohyu, and it is our belief that it is not taught or disclosed in Kiyuna.

As for claims 16 and 17, as argued above, it is our belief that Ohyu fails to disclose or teach of a processor configured to support multiple threads. It is also our belief that Kiyuna fails to disclose or teach about a processor configured to support multiple threads.

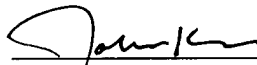
Consequently we respectfully request that claims 3-6, 13, 16, and 17 be allowed.

Conclusion

Applicant respectfully submits that, as amended, the subject application is in condition for allowance, and allowance thereof is kindly requested. Should the Examiner wish to discuss these claims further, or should an Examiner's Amendment be needed in order for the claims to proceed to allowance, the Examiner is invited to contact the undersigned attorney at the Examiner's earliest convenience.

Respectfully submitted,

Dated: February 13, 2006



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